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Download Free Komik Petruk Gareng PdfIn the manufacture of semiconductor devices, integrated circuits are formed on a semiconductor substrate, and can include transistors, memory cells, resistors, capacitors, and other devices such as diodes and interconnections. These devices are interconnected with each other and the interconnection lines are separated from each other to prevent shorting. Interconnection lines are formed by first depositing a dielectric layer over a semiconductor substrate then opening a contact or via hole through the dielectric layer to the semiconductor substrate. Thereafter, the contact or via hole is filled with a metal to form the interconnection line. A dielectric layer is then deposited over the interconnection line. A contact or via hole is etched through the dielectric layer to the interconnection line to form a second interconnection line. The interconnection lines are typically separated from each other by a dielectric layer. The dielectric layer has a high dielectric constant to electrically isolate the conductive interconnection lines from each other, and a relatively high thermal coefficient of expansion to accommodate thermal expansion and contraction of the semiconductor substrate. The dielectric layer is often a multilayer structure of dielectric materials to prevent the diffusion of metal ions into the dielectric material from the metal interconnection lines. In some applications of semiconductor devices, it is desirable to place the conductive interconnection lines closer to each other to decrease the size of the semiconductor device. The increased density allows more semiconductor devices to be manufactured within a semiconductor substrate of a given size. However, as the distance between the conductive interconnection lines becomes smaller, it becomes more difficult to manufacture a dielectric layer with a dielectric constant greater than about four, a thermal coefficient of expansion greater than about 15 ppm/degree Celsius, and a thickness less than about 5000 angstroms. Polyimides (sometimes called Kapton(trademark)) are used as a dielectric material for integrated circuits. Polyimides are typically used in dielectric layers as they can be deposited easily using conventional methods. However, polyimides have a dielectric constant of about five and a thermal coefficient of expansion of about 19 ppm/degree Celsius. Intermetal dielectrics are increasingly being used in integrated circuits to provide insulation between the interconnection lines and semiconductor substrate. However, intermetal dielectrics can also be formed with a

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